

The Myriad simulator: parallel computation for densely integrated models

| 1 Why and | other simulator? |
|--|---|
| Larger network models, especially many biophysical model inter- and intra-neuron interactions with multiple non-linear Such <i>densely integrated</i> circuits are poorly optimized in ge simulators, devolving into solving multiple stiff equations linear | |
| Modelers must of purpose simulator special coding to | ften trade performance for biophysical ac ors' implementations being difficult to para achieve limited multiprocessing capabilit |
| Myriad separates model design and code optimization into connected by powerful code generation middleware, enab multithreading capabilities on commodity hardware, GPUs | |
| 2 Design c | overview of the Myriad sim |
| | Idiomatic Python 3 with optional C optional |
| | High-level abstractions for neurons, and network properties. |
| User Code & Support Libraries (Python) | Mechanisms and other elements (p pumps, etc.) are user-definable with inheritance (e.g., channels inherit p ions that they flux). |
| | Simulations are represented as object parameter searches and reproducit |
| | Inheritance functionality via Python' Automatic access to properties of Functionality can be extended & |
| | Mako templates for converting user and Python-compatible structures for |
| Compilation & Data Transfer Middleware (CPython) | <u>Removes all hierarchy from compar</u> recognizing only two computational <i>Compartments</i> (isometric) with p <i>Mechanisms</i> that connect exactly exactly one direction. (Connection extracellular medium are a special |
| | AST-to-AST translation of Python content |
| | Each compartment or mechanism is t computational element, enabling par separate CPU threads or GPU CUDA updated via shared memory access. |
| Simulation Backend & | Synchronization via barrier intrinsics a safe access to shared data without ra |
| Optimization (C + CUDA C) | Separate compilation phase enables of aggressively inline mechanism and compilation |
| | POSIX message queues used as sign communicating data between the from executable via Unix Domain Sockets |



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http://www.gputechconf.com/ Carnevale NT, Hines ML (2006) The NEURON Book. Cambridge, UK: Cambridge University Press.

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5 User extension example: AMPA Synapse with STDP

Using Model 3 from Sjostrom, Turrigiano, & Nelson # Post-synaptic compartment spike threshold post spike thresh = Unit(mV, 10.0) pre_spike_thresh = Unit(mV, 10.0) # State to keep track of time each compartment fired P synaptic strengthening factor naptic weakening factor

Synaptic weight and maximum weight value

def calc_current(self, pre_syn_comp, post_syn_comp, step, global_time) # We can get the AMPA current calculation from our parent I_AMPA = super().calc_current(pre_syn_comp, post_syn_comp) # Weight change (to be calculated)

Get post-synaptic cell's current/previous membrane voltage post_vm_curr = post_syn_comp.vm[step] post_vm_prev = post_syn_comp.vm[step - 1] # If the post-synaptic cell has spiked if (post_vm_curr >= self.post_spike_thresh and post_vm_prev <= self.post_spike_thresh):</pre> # If the presynaptic spike is in if (global_time - self.t_pre <= self.t_ltp_edge):</pre> delta_t = m_fabs(self.t_pre - global_time) d_omega = self.a_plus * m_exp(delta_t / self.tau_plus) self.t_post = -INFINITY # Otherwise, just set the time we fired (current global time) self.t_post = global_time # Get pre-synaptic cell's current/previous membrane voltage pre_vm_curr = pre_syn_comp.vm[step] pre_vm_prev = pre_syn_comp.vm[step - 1] # If the pre-synaptic cell has spiked if (pre_vm_curr >= self.pre_spike_thresh and pre_vm_prev <= my_stdp_ampa_curr->pre_spike_thresh): # If the postsynaptic spike is in our window, weaken the weight if (global_time - self.t_post <= self.t_ltd_edge and</pre> self->t_pre < self->t_post): delta_t = m_fabs(self.t_post - global_time) d_omega = self.a_plus * m_exp(delta_t / self.tau_plus) self.t_post = -INFINITY self.t_pre = global_time # Alter the weight based on above calculations self->omega += delta_omega # Bound the weight between 0 and omega_max

if (self.omega < 0.0):</pre> self.omega = 0.0; elif (self.omega > self.omega_max);

self.omega = self.omega_max

Return the current times the synaptic weight return I AMPA * self->omega

This example inherits from a previously-defined, out-of-source AMPA synapse **mechanism**.

Here, the user defines several state variables that are *owned* by the STDPAMPASyn mechanism, where each instance has its own copy.

Default values are provided here, though these are optional. The constructor is elided by the middleware.

Values are expressed as either of *Unit* or *MDouble*, a generic unitless type.

The parser uses the @myriad_method annotation (highlighted in a red rectangle) in order to know which methods to convert at parse-time.

Planned Extensions

Docker support for facilitating deployments is planned on release

Implement simulation governor to run multiple instances in series or in parallel (e.g., on distributed-architecture GPU clusters), to support parameter exploration and algorithmic optimization.

Myriad is an arbitrarily programmable GPU-enabled computational framework that is in principle as appropriate for (e.g.) 3-D spatial diffusion models as for neuronal modeling. Assess Myriad's utility for these different applications, and their synthesis.

Extend Myriad to a nonuniform memory access architecture to support multiple CUDA cards on a single high-speed bus (NVLink).

7 References & Acknowledgments

Rittner P, Cleland TA (2014) The MYRIAD simulator: densely coupled realistic neural networks on GPU. GPU Technology Conference, San Jose, CA.